DO-254 and DO-178C Acronym List

The following acronyms are commonly used in the context of DO-254 programs. DO-254 program participants should familiarize themselves with this list.

- **AC:** (FAA) Advisory Circular
- **AEH:** Airborne Electronic Hardware
- **AMC:** (EASA) A Means of Compliance
- **ARP:** (SAE) Aerospace Recommended Practice
- **ASA:** Aircraft Safety Assessment
- **ASIC:** Application Specific Integrated Circuit
- **BOM:** Bill of Materials
- **CAAC:** Civil Aviation Administration of China
- **CCA:** Common Cause Analysis
- **CBA:** Circuit Board Assembly (same as PCB)
- **CCB:** Configuration Control Board
- **CEH:** Complex Electronic Hardware
- **CFR:** Code of Federal Regulations
- **CM:** Configuration Management, or Certification Memo
- **COTS:** Commercial Off The Shelf (devices from 3rd party vendors)
- **COTS IP:** COTS Intellectual Property
- **CPLD:** Complex PLD (Programmable Logic Device)
- **CRAM:** Configuration RAM (Random Access Memory
- **CRI:** (EASA) Certification Review Item
- **DAL:** Design or Development Assurance Level
- **DER:** Designated Engineering Representative
- **DO:** (RTCA) Document
- **DOA:** (EASA) Design Organisation Approval
- **EASA:** European Aviation Safety Agency (the EU equivalent of the FAA)
- **ECMP:** Electronic Component Management Plan
- **ED:** (EASA) EUROCAE Document
- **EUROCAE:** European Organisation for Civil Aviation Equipment
- **ETSO:** European TSO (Technical Standard Order)
- **FAA:** Federal Aviation Agency
- **FDAL:** Functional DAL (Design or Development Assurance Level)
- **FHA:** Functional Hazard Assessment
- **FIT:** Failure in Time
- **FPGA:** Field Programmable Gate Array
- **FTA:** Fault Tree Analysis

- **HAS:** Hardware Accomplishment Summary
- **HCI:** Hardware Configuration Index
- **HCMP:** Hardware Configuration Management Plan
- **HDD:** Hardware Design Description
- **HDL:** Hardware Description Language
- **HDP:** Hardware Design Plan
- **HDS:** Hardware Design Standards
- **HECI:** Hardware Environment Configuration Index
- **HPA:** Hardware Process Assurance
- **HPAP:** Hardware Process Assurance Plan
- **HRD:** Hardware Requirements Document
- **HRS:** Hardware Requirements Standards
- **HVR:** Hardware Verification Results
- **HVVP:** Hardware Validation and Verification Plan
- **HVVS:** Hardware Validation and Verification Standards
- **HW:** Hardware
- **IC:** Integrated Circuit
- **I/O:** Input/output
- **IDAL:** Item DAL
- **IP:** Intellectual Property, or sometimes Issue Paper
- **MOC:** Means of Compliance
- **ODA:** Organizational Delegation Authority
- **OEM:** Original Equipment Manufacturer
- **PA:** Process Assurance (DO-254)
- **PASA:** Preliminary Aircraft Safety Assessment
- **PCB:** Printed Circuit Board
- **PDH:** Previously Developed Hardware
- **PHAC:** Plan for Hardware Aspects of Certification (pronounced "Pee-Hack")
- **PLD:** Programmable Logic Device
- **PSAC:** Plan for Software Aspects of Certification (pronounced "Pee-Sack")
- **PSE:** Product Service Experience (used in the context of COTS in the EASA memo)
- **PSSA:** Preliminary System Safety Assessment
- **QA:** Quality Assurance (DO-178C)
- **RAM:** Random Access Memory
- **ROM:** Read Only Memory
- **RTCA:** Radio Technical Commission for Aeronautics
- **RTL:** Register Transfer Level
- **RTM:** Requirements Traceability Matrix
- **SAS:** Software Accomplishment Summary

- **SC:** (RTCA) Special Committee
- **SCI:** Software Configuration Index
- **SCMP:** Software Configuration Management Plan
- **SCS:** Software Code Standards
- **SDD:** Software Design Document
- **SDP:** Software Design Plan
- **SDS:** Software Development Standards
- **SEE:** Single Event Effects
- **SEH:** Simple Electronic Hardware
- **SEM:** Soft Error Mitigation
- **SEU:** Single Event Upset
- **SoC:** System on Chip
- **SOI:** Stage of Involvement (an audit by FAA designated personnel)
- **SQAP:** Software Quality Assurance Plan
- **SRAM:** Static Random Access Memory
- **SRD:** Software Requirements Document
- **SRS:** Software Requirements Standards
- **SSA:** System Safety Analysis
- **SVCP:** Software Verification Cases and Procedures
- **SVR:** Software Verification Results
- **SVVP:** Software Verification and Validation Plan
- **SW:** Software
- **TC:** Type Certificate
- **TSO:** Technical Standard Order
- **VCI:** Verification Configuration Index
- **VHDL:** Very High Speed Integrated Circuit Hardware Description Language
- **WG:** (EUROCAE) Working Group