The Greatest Operational Amplifier Course on the Internet.





Part One Linear Operational Amplifier Part One

Operatinal Amplifiers from Soup to Nuts Al's Electronic Class Room

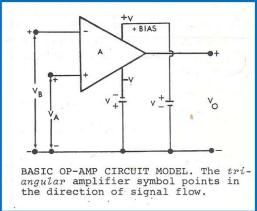
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- Operational Amplifier or OP-Amp
- There are three basic Operational Configurations
 - Inverting
 - Non-Inverting
 - Differential
- We will be discussing the Linear Operational Amplifier First

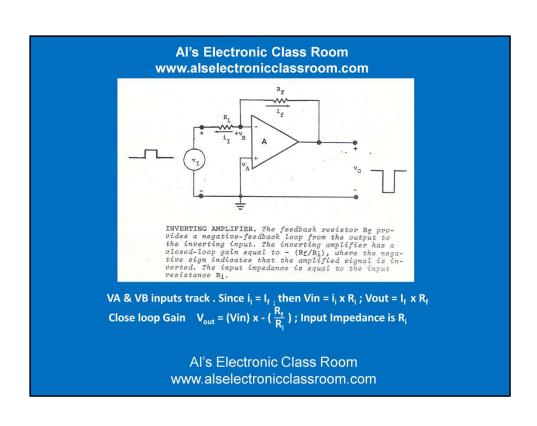
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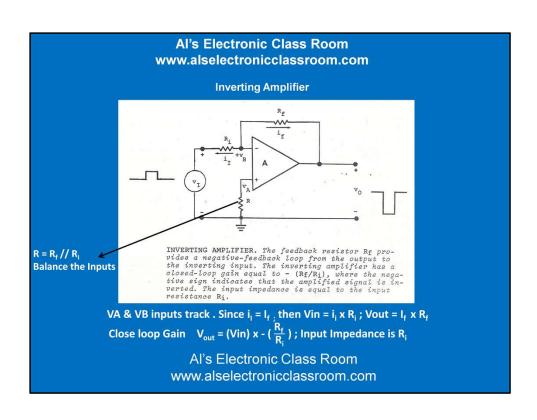
- BASIC OP-AMP CIRCUIT MODEL. The triangular amplifier symbol points in the direction of signal flow.
- Standard Operational Symbol
- Two Inputs , Inverting+ Non _Inverting
- Output is taken at the Single Output and V_o to Gnd.
- The Operational Amplifier has Two Voltage Sources. +V & -V
- V₀ = A(V_A V_B) (Where A is open loop Gain of the Operational Amplifier) (Op-Amp)
- Typical Open Loop Gain is around 50000.

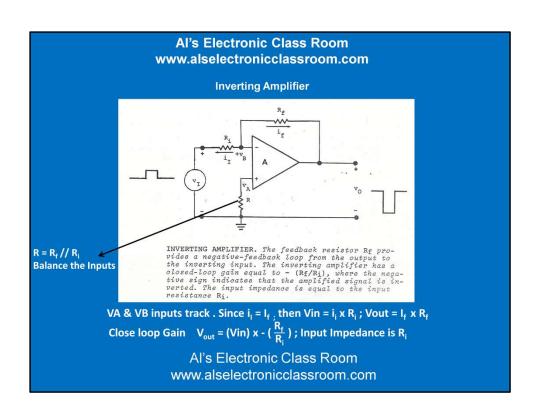
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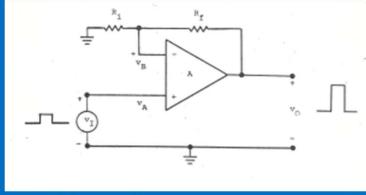
- Output of the Operational Amplifier, Cannot have an output greater then either of the Supply Voltage Sources.
 +V & -V.
- Since the typical voltage supply Voltage for the Op --Amp is +/- 15 Volts DC
 Therefore (0.3 M Volts) x 50000
 = 15 VDC; Small input Voltage will hit the rail of the Power supply.



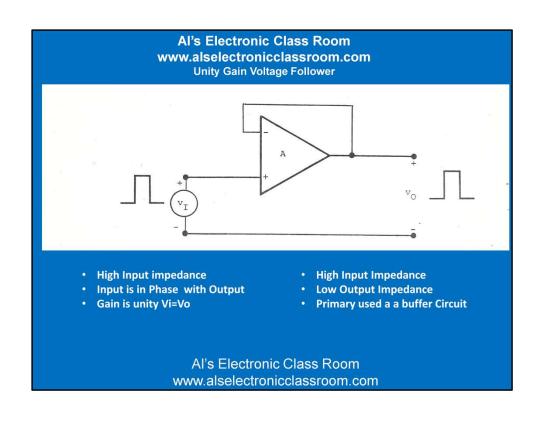


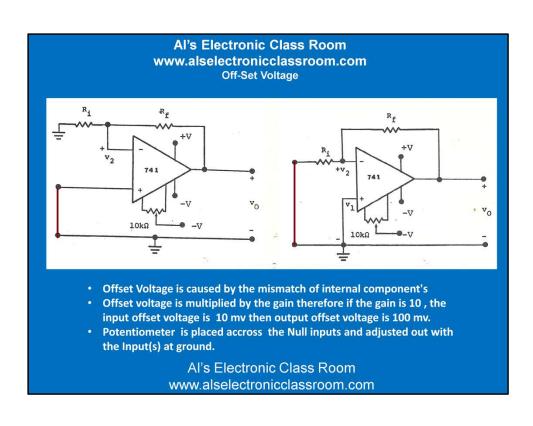


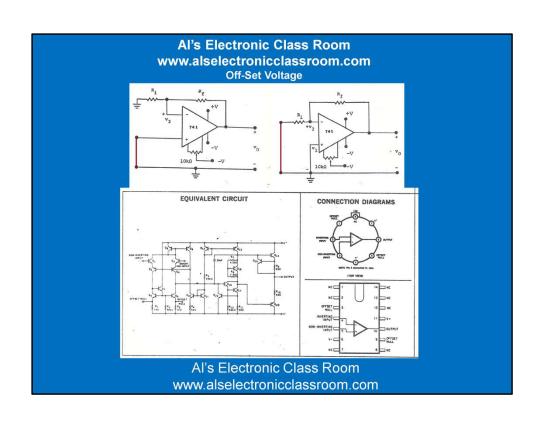
Al's Electronic Class Room www.alselectronicclassroom.com Non-Inverting Amplifier

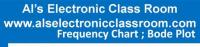


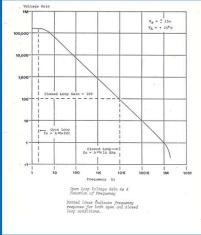
- High Input impedance
 Input is in Phase with Output
 Gain is 1 + ^R_I = Output
 Also Called a Voltage Follower
- Low Output Impedance
- Primary used a buffer Circuit





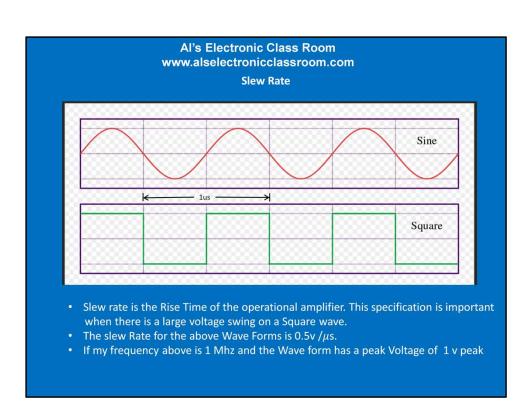


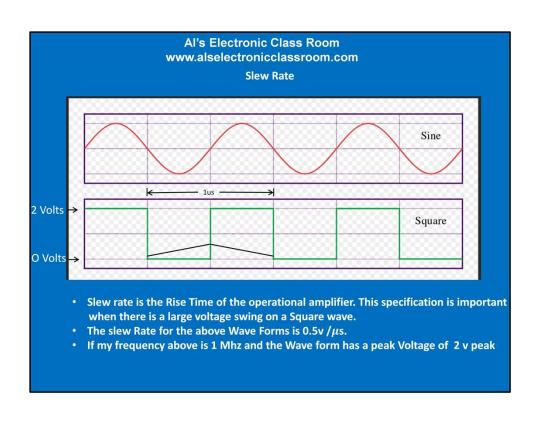




- Open loop gain is about 2 at 2 Htz rolls of at a factor of ten as frequency is increased by a factor of 10 or 10 Db per octave (Meaning gain drops by $\frac{1}{2}$ when frequency dbls)
- Close loop gain is 100 roll off starts at 10 khz, then continues to Roll off at the same rate as open loop gain.

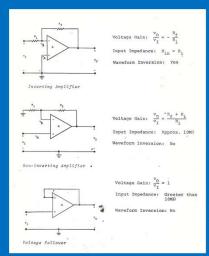
| Slew Rate FAIRCHILD LINEAR INTEGRATED CIRCUITS μ A741C ELECTRICAL CHARACTERISTICS (V ₅ = ±15 V, T ₄ = 25 °C unless otherwise specifie6) | | | | | | | | | | | |
|--|---|---------------|---------|------|------------|------------------------------|-----------------------|------|------|------|-------|
| | | | | | | PARAMETERS (see definitions) | CONDITIONS | MIN, | TYP, | MAX. | UNITS |
| | | | | | | Input Offset Voltage | $R_S \le 10 k\Omega$ | | 2.0 | 6.0 | mV |
| | | | | | | Input Offset Current | | | 20 | 200 | nA |
| Input Bias Current | | | 80 | 500 | nA | | | | | | |
| Input Resistance | | 0.3 | 2.0 | | MO | | | | | | |
| Input Capacitance | | | 1.4 | | pF | | | | | | |
| Offset Voltage Adjustment Range | | | ±15 | | mV | | | | | | |
| Input Voltage Range | | ±12 | ±13 | | V | | | | | | |
| Common Mode Rejection Ratio | $R_5 \leq 10 \text{ k}\Omega$ | 70 | 90 | | dB | | | | | | |
| Supply Voltage Rejection Ratio | $R_S \leq 10 \text{ k}\Omega$ | | 30 | 150 | 1/V | | | | | | |
| Large-Signal Voltage Gain | $R_L \ge 2 \text{ km}, V_{o,t} = \pm 10 \text{ V}$ | 20,000 | 200,000 | * | | | | | | | |
| Output Voltage Swing | $R_L \ge 10 \text{ km}$ | ±12 | ±14 | | ٧ . | | | | | | |
| | $R_L \ge 2 \text{ k}\Omega$ | ±10 | ±13 | | ٧ | | | | | | |
| Output Resistance | | | 75 | | 11 | | | | | | |
| Output Short-Circuit Current | | | 25 | | mA | | | | | | |
| Supply Current | | | 1.7 | 2.8 | mA | | | | | | |
| Power Consumption Transient Response (unity gain) | | | 50 | 85 | mW | | | | | | |
| Risetime (unity gain) | $V_{\rm in}=20$ mV, $R_{\rm i}=2$ kΩ, $C_{\rm i}\leq 100$ pF | | 0.3 | | | | | | | | |
| Overshoot | | | 5.0 | | 145 96 | | | | | | |
| Siew Rate | $R_{\rm L} \ge 2 k\Omega$ | | 0.5 | | 76 V/45 | | | | | | |
| The following specifications app | | | 0.5 | | 4/112 | | | | | | |
| | y 101 0 0 5 1 5 + 10 0; | | | 7250 | | | | | | | |
| Input Offset Voltage Input Offset Current | | | | 7.5 | mV | | | | | | |
| Input Bias Current | | | | 300 | nA | | | | | | |
| The state of the s | 8 > 2 to V - +10 V | 15 000 | | 800 | nA | | | | | | |
| | | | . 12 | | | | | | | | |
| erge-Signal Voltage Gain utput Voltage Swing | $R_L \ge 2 \text{ k}\Omega$, $V_{out} = \pm 10 \text{ V}$ $R_L \ge 2 \text{ k}\Omega$ | 15,000 ±10 | ±13 | 1000 | ٧ | | | | | | |



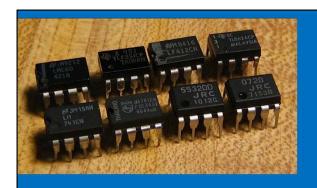


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Linear Op Amp Summary



- Voltage Gain = Vi = Rf/Ri
 Input Impedance = Ri
 Inversion yes = input; output by 180 degrees
- Voltage Gain = Vi = 1 + Rf/Ri
 Input Impedance = High Input Impedance
- Inversion no = input; output in Phase
- Voltage Gain = 1
- Input Impedance = High Input Impedance
- Inversion no = input; output in Phase



Continue to Part two Differential Operational Amplifier

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